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**RESPONSE UNDER 37 C.F.R. 1.116 - EXPEDITED  
PROCEDURE - EXAMINING GROUP 2824  
PATENT**

Attorney's Docket No. 5649-944

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re: Ahn et al.  
Serial No.: 10/074,309  
Filed: February 11, 2002  
For: **INTEGRATED CIRCUIT DEVICES HAVING DELAY CIRCUITS FOR  
CONTROLLING SETUP/DELAY TIMES OF DATA SIGNALS THAT ARE  
PROVIDED TO MEMORY DEVICES AND METHODS OF OPERATING SAME**

Confirmation No.: 6441  
Group Art Unit: 2824  
Examiner: Jung H. Hur

Date: January 21, 2004

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

Applicants provide the present Amendment to address the issues raised in the Final Official Action mailed October 22, 2003. Applicant provides the present Amendment pursuant to the rules stated in revised 37 C.F.R. 1.121 that became effective on July 30, 2003.

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